

Barbaro Marano

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Personal Details

Date and Place of Birth: 01ST October 1984, Catania, Italy

Gender: Male

Nationality: Italian

Education

- 07/2009 . 03/2011 **"Università degli studi di Catania" & "ST Microelectronics S.r.l", Catania (CT), Italy**
Ph.D Student in Electronic Engineering, Automation and Control of Complex Systems
- Subject: Integrated solution for efficient power galvanic isolation (**Patent Applied in Italy: PA72232**)
- 10/2006 . 07/2009 **"Università degli studi di Catania", Catania (CT) Italy**
Master of Science in Microelectronic Engineering
- Final Grade: **110 out of 110 *summa cum laude***
 - GPA: 29.48 out of 30
 - Thesis: Design of a RFID far-field antenna magnetic-coupled to the IC
- 10/2003 . 10/2006 **"Università degli studi di Catania", Catania (CT) Italy**
Bachelor of Science in Electronic Engineering
- Final Grade: **110 out of 110**
 - GPA: 27.72 out of 30
 - Thesis: Design and modeling of integrated passive devices

Work Experiences

- 03/2011 . *Present* **"Accenture S.p.A", Milan (MI), Italy**
Analyst - Management Consulting
- Assist in defining requirements and designing applications to meet business process and application requirements.
- 10/2003 . 09/2009 **"Karting Club - Pista S.Venera", Acireale (CT), Italy**
(part-time) **Coordinator**
- Organization and management of sporting events

Professional Competencies

- Isolated VHF DC-DC converters, ZVS Resonant oscillators, High efficiency rectifiers
- Discrete component design (Variable Gain Amp., Phase shifter, High precision rectifier)
- Electrostatic Discharge and latch-up immunity design knowledge
- Background in Galvanic Isolation
- IC Layout experience, LVS, DRC, PLS
- Development of an optimization tool in Matlab to assist design of resonant transformers
- 2D/3D Electromagnetic passive devices simulation and customization (high-Q inductors, transformers, antennas)
- Device models and netlist syntax understanding
- Design, fabrication and assembly of an innovative leadless UHF RFID Tag
- Several technologies used (BCD6s, BCD6SOI, BCD8, CMOS090)
- Top level and architecture definition
- Experience on laboratory activity (oscilloscopes, spectrum analyzers, multi-meters, power units)
- Experience on probe station analysis of ICs

Languages

- Italian: Mother Tongue
- English: *Upper Intermediate Level* for "EF Language Learning Solutions®" (Accenture® English Test)

CAD tools

- Cadence: ICFB, Virtuoso Schematic Editor, Virtuoso Analog Design Environment, PLS
- Mentor Graphics: Eldo simulator, EZwave waveform viewer
- Agilent: ADS (Advanced Design System), Momentum, EMPro 3D
- Ansys: HFSS, Designer
- Mathworks: Matlab
- Labcenter Electronics: Proteus Professional, Proteus VSM Studio IDE

In accordance to the Italian privacy current regulation (D. Lgs. 196/2003), I hereby declare to authorize my personal data